



SDM-300 Modem

Bulk FW Release 9330N

Software Release Notification

SDM-300 Modem, FW/9330N Release

Approval Signatures	
Product Manager: Jeff Harig	Originator: John Markowski

Applicability
Bulk firmware upgrade for previous releases of SDM-300L3 Modems.

Rev	Description	Revision History		
		FPGA	Date	Prepared by
M	Release of FW/9330M	645C	03/30/06	J.Markowski
N	Release of FW/9330N	D235	05/25/06	J.Markowski

FSCM No. 4J515

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Introduction

The primary purpose of this release is to correct a timing issue at high data rates which could cause data errors.

New Features

None.

Issues Addressed In This Release

- Corrected a timing issue that could cause data errors when the modem was operating at high data rates (above 2.0 Mbps) when modulation type was QPSK or 8PSK. This timing issue was revealed when changing the manufacturer of the Rx buffer RAM IC in order to comply with the RoHS directive.

Firmware Compatibility

The following table indicates the M&C and Bulk FPGA firmware compatibility. Using configurations other than those indicated, may result with the modem not operating as expected.

SDM-300 L3 Firmware Compatibility Table						
M&C		FPGA		BOOT		HW ECOs
FW/8460-1AH	v2.2.5	FW/9330L	N/A	FW/9329A	v1.1.2	
FW/8460-1AJ	v2.2.6	FW/9330L	N/A	FW/9329A	v1.1.2	# 25222 HW
FW/8460-1AJ	v2.2.6	FW/9330M	N/A	FW/9329A	v1.1.2	
FW/8460-1AJ	v2.2.6	FW/9330N	*	FW/9329A	v1.1.2	# 27180 HW

- * **NOTE:** The following hardware ECO information applies only to a TX/RX SDM-300L3 modem and does not apply to a RX or TX only modem:

Hardware ECO # 27180 (05/26/06) required for proper operation at high data rates. This ECO consists of modifying prior ECO # 25222 (05/24/05) which is the undo of the jumper wire defined in # 25222 and adding a new jumper wire from U112 pin 74 to test point E40 (which connects to U13 pin 165). Refer to ECO # 27180 for proper routing instructions.

Known Issue(s) In This Release

None.